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Page(s): 818 -821 vol.2

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13: (12) (phase adj locked adj loop or PLL) and tuning adj voltage and leakage adj current

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UDC

Queue

Trash

DB+USPAT:US PGPUB:EPD:PO:DERWENT:IBM:TDE

Details operator:OR

(phase adj locked adj loop or PLL) and tuning adj voltage and leakage adj current

BRD form

SA form

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1			US 20030060618 A1	20030501	15	Fast-acquisition phase-locked loop	331/17			McCarthy, Evan S.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
2			US 20030053576 A1	20030320	27	Linear phase detector for high-speed clock and data recovery	375/375	375/376		Cao, Jun	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
3			US 20010050597 A1	20011213	8	Oscillating circuit	331/1080	331/117R; 331/167		Ohgura, Hirokazu et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
4			US 20010035407 A1	20011025	20	Linear full-rate phase detector and clock and data recovery circuit	398/55	327/152		Cao, Jun	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
5			US 6456096 B1	20021015	9	Tunable oscillator with leakage compensation	331/11	327/156; 327/157;		DeVito, Lawrence M.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
6			US 6437651 B1	20020820	7	Oscillating circuit	331/108C	331/117; 331/105; 331/175; 331/185		Ohgura, Hirokazu et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
7			US 6157271 A	20001205	7	Rapid tuning, low distortion digital direct modulation phase locked loop and method therefor	332/127	331/16; 331/17; 331/274		Black, Gregory Redmond et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
8			US 6150806 A	20001121	9	Controlled current source for accelerated switching	323/315	323/275		Canard, David et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
9			US 6091943 A	20000718	11	Combining oscillator with a phase-indexed control circuit for a radio receiver	455/260	375/376; 455/150.1; 455/76		Nyenhus, Detlev	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
10			US 5920233 A	19990706	20	Phase locked loop including a sampling circuit for reducing spurious side bands	331/14	331/17; 331/25; 331/110.2		Denny, Paul A.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
11			US 5537448 A	19950716	7	Switchable PLL circuit with constant amplification gradient	375/376	375/223; 375/327; 375/371		Schwarz, Detlef et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	

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